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2814

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Mahajani et al.

Application No.: 10/079,472

Group Art Unit: 2814

Filed: 2/19/2002

Title: Gate Dielectric Structures for
Integrated Circuits and Methods for Making
and Using Such Gate Dielectric Structures

Examiner: Thao X. Le

Attorney Docket No.: MA-068

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

OFFICE ACTION RESPONSE

Dear Sir:

Please enter this response in reply to the Office Action of March 12, 2004 in the above-captioned case.

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Pamela J. Squyres
Date of Mailing 4/23/04

App No. 10/079,472



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(to be used for all correspondence after initial filing)

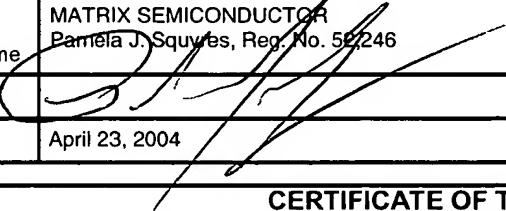
Total Number of Pages in This Submission

43

Application Number	10/079472
Filing Date	February 19, 2002
First Named Inventor	Maitreyee Mahajani
Art Unit	2814
Examiner Name	Thao X. Le
Attorney Docket Number	MA-068

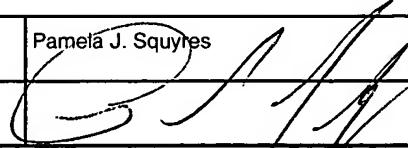
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	MATRIX SEMICONDUCTOR Pamela J. Squyres, Reg. No. 52,246
Signature	
Date	April 23, 2004

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Date	April 23, 2004

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